

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	87	(MDIO (managment adj data and input adj output)) and pad	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L2	1	"4833605".pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L3	1	"6259693".pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L4	1	"20040117698".pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L5	2812	serdes	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L6	1903	L5 and ((serial and parallel) and ((serial with serial) (serial with parallel) (paralle with parallel)))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L7	930	L6 and (IEEE ("802"\$4) XGMII TBI RTBI HSTL SSTL LVTTTL MDIO)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L8	663	L6 and (("802"\$4) XGMII TBI RTBI HSTL SSTL LVTTTL MDIO)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L9	331	L6 and (("802.3") XGMII TBI RTBI HSTL SSTL LVTTTL MDIO)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L10	331	L9 and (pin pad port pads pins ports link connect\$4)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L11	251	L10 and (transceiver resceiver)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L12	29	L11 and "714"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L13	259	L9 and ((pin pad port pads pins ports link connect\$4) with (program \$5 Configur\$4))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L14	23421	(serdes serializer deserializer serializ\$4 deserializ\$4)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L15	1969460	(transceiver receiver transmitter transceiv\$4 receiv\$4 transmit\$4).clm.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22

L16	2264955	(transceiver receiver transmitter transceiv\$4 receiv\$4 transmit\$4 communicating communication communicat\$4).clm.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L17	17854	L14 and L16	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L18	10331	L17 and (protocols XGMII XGMI TBI RTBI (ten adj bit adj interface) (reduced adj ten adj bit adj interface))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L19	10153	L18 and (pin pad port pads pins ports link connect\$4)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L20	6622	L19 and ((electrical adj specification) specification HSTL SSTL LVTTL (High adj speed adj transistor adj logic) (solid adj state adj track adj link) (low adj voltatge adj transistor adj transistor adj logic) (low adj voltatge adj transistor adj transistor adj logic) (IEEE with ("802.3" "802")))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L21	3651	L20 and ((serial and parallel) and ((serial with serial) (serial with parallel) (paralle with parallel)))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L22	949	L21 and (((serial) near2 (pin pad port pads pins ports link connect\$4)) and ((parallel) near (pin pad port pads pins ports link connect\$4)))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L23	753	L22 and ((protocols XGMII XGMI TBI RTBI (ten adj bit adj interface) (reduced adj ten adj bit adj interface)) same (pin pad port pads pins ports link connect\$4))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L24	599	L23 and ((pin pad port pads pins ports link connect\$4 MDIO (management adj data adj ((input/output) (input adj output)))) with (program\$5 Configur\$4))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22

L25	533	L22 and (((electrical adj specification) specification HSTL SSTL LVTTTL (High adj speed adj transistor adj logic) (solid adj state adj track adj link) (low adj voltatge adj transistor adj transistor adj logic) (low adj voltatge adj transistor adj transistor adj logic) (IEEE with ("802.3" "802")) same (pin pad port pads pins ports link connect\$4))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L26	460	L25 and ((pin pad port pads pins ports link connect\$4 MDIO (management adj data adj ((input/output) (input adj output)))) with (program\$5 Configur\$4))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L27	157	L26 and (BERT (bit adj error adj rate adj test\$3) (bit adj error adj rate) "714"/\$.ccls.)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L28	5	("4833605" "6259693" "6317804" "7035228" "7032139").pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L29	1	("6483849").pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L30	7	("5726991" "6140956" "20030009307" "20030048781" "6438717" "20020054569" "20010012288").pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L31	1	("6081570").pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L32	1	("4967201").pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L33	1	("4833605").pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L34	1	("6259693").pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L35	1	("20040088443").pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L36	1	("20040088444").pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L37	1	("20050190690").pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22

L38	1	("20050094734").pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L39	1	("20060250985").pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L40	1	"6317804".pn.	USPAT	OR	ON	2009/01/12 11:22
L41	29	((transistor transistor) near2 logic) (track near2 link) HSTL SSTL LVTTL) and (TBI RTBI XGMII GMII MII (ten adj bit adj interface) (reduced TBI) (Medium adj independent adj interface)) and (MDIO (management adj data adj (input (input/ output) (input\$output))))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L42	13	L41 and ((IEEE with ("45" and "22")))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L43	54	((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTL) and (TBI RTBI XGMII GMII MII (ten adj bit adj interface) (reduced TBI) interface (Medium adj independent adj interface)) and (MDIO (management adj data adj (input (input/ output) (input\$output))))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L44	56	((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTL) and (TBI RTBI XGMII GMII MII (ten adj bit adj interface) (reduced TBI) interface (Medium adj independent adj interface)) and (MDIO (management adj data adj (input (input/ output) (input\$output) input output input\$7)))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L45	1	(interface adj controll\$4) same ((parallel serial) near2 (port connection connection\$4)) and (MDIO (management adj data adj (input (input/ output) (input\$output) input output input\$7)))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22

L46	89	(interface adj controll\$4) and (MDIO (management adj data adj (input (input/output) (input\$output) input output input\$7)))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L47	13	L46 and (((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL and (TBI RTBI XGMII GMII MII (ten adj bit adj interface) (reduced TBI) interface (Medium adj independent adj interface))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L48	57	(((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL and (TBI RTBI XGMII GMII MII (ten adj bit adj interface) (reduced TBI) interface (Medium adj independent adj interface)) and (MDIO (management adj data adj (input (input/output) (input\$output))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L49	5110	(((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL and (transceiver transceiver)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L50	2548	(((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL) same (two "at least" "at-least" number plurality)) and (transceiver transceiver)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L51	1467	(((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL) same (two "at least" "at-least" number plurality) same (pin pad port connection)) and (transceiver transceiver)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L52	96	(((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL) same (two "at least" "at-least" number plurality) same (pin pad port connection)) same (transceiver transceiver)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22

L53	2460	(((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL) same (pin pad port connection)) and (transceiver transceiver)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L54	1809	(((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL) with (pin pad port connection)) and (transceiver transceiver)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L55	1256	(((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL) with (pin pad port connection)) same (two "at least" "at-least" number plurality multiple multitude)) and (transceiver transceiver)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L56	183	(((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL) with (pin pad port connection)) with (two "at least" "at-least" number plurality multiple multitude)) and (transceiver transceiver)	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L57	244	(((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL) with (pin pad port connection)) and (transceiver transceiver) and (((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL) with (two "at least" "at-least" number plurality multiple multitude))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22

L58	21	(((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced TBI) (Medium adj independent adj interface)) same ((programmable) near2 (pin pad port connection))) and (transceiver transceivor) and (((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced TBI) (Medium adj independent adj interface)) with (two "at least" "at-least" number plurality multiple multitude))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L59	21	(((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced TBI) (Medium adj independent adj interface)) same ((programmable) near2 (pin pad port connection contact))) and (transceiver transceivor) and (((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced TBI) (Medium adj independent adj interface)) with (two "at least" "at-least" number plurality multiple multitude))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L60	34529	"710"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22

L61	12133	L60 and (((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced TBI) (Medium adj independent adj interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L62	2852	L60 and (((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced TBI) (Medium adj independent adj interface)) same (pin pad port connection contact))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L63	1247	L60 and (((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced TBI) (Medium adj independent adj interface)) same (pin pad port connection contact)) and ((two "at least" "at-least" number plurality multiple multitude) with (((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced TBI) (Medium adj independent adj interface)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L64	792	L60 and (((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced TBI) (Medium adj independent adj interface)) with (pin pad port connection contact)) and ((two "at least" "at-least" number plurality multiple multitude) with (((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22

		LVTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced TBI) (Medium adj independent adj interface)))				
L65	29	L64 and (((programmable MDIO (management adj data adj (input (input/ output) (input\$output) (input near2 output)))) near2 (pin pad port connection contact))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L66	110	L60 and (((((transistor transister) near2 logic) ttl (track near2 link) HSTL SSTL LVTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced adj TBI) (Medium adj independent adj interface)) with (pin pad port connection contact)) and ((two "at least" "at- least" number plurality multiple multitude) with (((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced adj TBI) (Medium adj independent adj interface)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L67	1437	L60 and (((((transistor transister) near2 logic) ttl (track near2 link) HSTL SSTL LVTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced adj TBI) (Medium adj independent adj interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L68	42	L67 and (((programmable MDIO (management adj data adj (input (input/ output) (input\$output) (input near2 output)))) near2 (pin pad port connection contact))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22

L69	142184	((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced adj TBI) (Medium adj independent adj interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L70	925	L69 and ((programmable MDIO (management adj data adj (input (input/output) (input\$output) (input near2 output)))) near2 (pin pad port connection contact))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L71	70	L69 same ((programmable MDIO (management adj data adj (input (input/output) (input\$output) (input near2 output)))) near2 (pin pad port connection contact))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L72	1	((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced adj TBI) (Medium adj independent adj interface)) and "20030110344".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L73	875302	Broadcom Corporation. as.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L74	5329	("Broadcom Corporation").as.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L75	5329	("Broadcom Corporation").as.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L76	728	L69 and L75	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22

L77	278	L76 and (parallel and serial)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L78	208	L77 and (programmable MDIO (management adj data adj (input (input/output) (input\$output) (input near2 output))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L79	5	"10694788"	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L80	9001	(two "at least" "at-least" number plurality multiple multitude) near9 (((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced adj TBI) (Medium adj independent adj interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L81	130	L80 and ((programmable MDIO (management adj data adj (input (input/output) (input\$output) (input near2 output)))) near2 (pin pad port connection contact))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L82	79	L81 and (communicat\$4 transceiver)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L83	6	("7034570" "6714050" "5999021" "6087851" "20040068593" "6172524").pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L84	0	L83 and ((programmable MDIO (management adj data adj (input (input/output) (input\$output) (input near2 output)))) near2 (pin pad port connection contact))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L85	134530	"370"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22

L86	5442	L85 and (((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced adj TBI) (Medium adj independent adj interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L87	98	L86 and ((programmable MDIO (management adj data adj (input (input/output) (input\$output) (input near2 output)))) near2 (pin pad port connection contact))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L88	14	L86 and (((programmable MDIO (management adj data adj (input (input/output) (input\$output) (input near2 output)))) near2 (pin pad port connection contact)) same (((transistor transistor) near2 logic) ttl (track near2 link) HSTL SSTL LVTTTL XGMII GMII MII TBI RTBI (ten adj bit adj interface) (reduced adj TBI) (Medium adj independent adj interface)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 11:22
L89	31	("4627070" "4833605" "4833695" "4967201" "5726991" "5793990" "5933021" "6081570" "6140956" "6259693" "6137734" "6317804" "6438717" "6483849" "7032139" "7035228" "20010012288" "20010015664" "20010021953" "20010017595" "20020054569" "20030009307" "20030048781" "20030120791" "20030172332" "20040083077" "20040088443" "20040141497" "20040141531" "20050190690" "20060250985").pn.	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22

L90	7	L89 and "802"\$3	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L91	6	L89 and (mdio (management near2 data))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22
L92	5	L89 and ((programmable program\$5) near3 (pin pad port))	US-PGPUB; USPAT	OR	ON	2009/01/12 11:22

1/12/2009 11:29:45 AM

C:\Documents and Settings\sradosevich\My Documents\EAST\Workspaces\10-694730.wsp